

GR 97 P 6457 P



#11/Amata  
April  
1/2/02

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By:

Date:

Nov 29, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jörg Berthold et al.  
Applic. No. : 09/595,860  
Filed : June 16, 2000  
Title : Integrated Electrical Circuit and Method for  
Fabricating It  
Examiner : Cuong Q Nguyen  
Group Art Unit : 2811

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,  
Washington, D. C. 20231

Sir :

Responsive to the Office action dated August 29, 2001 kindly  
amend the above-identified application as follows:

In the Specification:

Please change the title to --INTEGRATED CIRCUIT HAVING A  
DIFFUSION BLOCKER CONFIGURED AS A BLOCKER LAYER AND CONNECTION